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Application No.: 10/702,372

APR - 6 2006

Docket No.: JCLA7897-D

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner: STARK, JARRETT J

Group Art Unit: 2823

In re PATENT APPLICATION of

Applicants : Ker et al.

Serial No. : 10 / 702,372

Filed : November 5, 2003

For : ELECTROSTATIC DISCHARGE
PROTECTION CIRCUIT OF NON-GATED
DIODE AND FABRICATION METHOD
THEREOFAMENDMENT

4/17/06
Enter
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Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The final Office Action mailed on February 2, 2006 has been carefully considered. In response thereto, please enter the following amendments and consider the following remarks.

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